JSS Mahavidyapeetha

JSS Academy of Technical Education

JSS campus, Dr.Vishnuvardhan Road, Srinivasapura, Bengaluru.
(AFFILIATED TO VTU BELAGAVI AND APPROVED BY AICTE,NEW DELHI)

Department of Electronics & Instrumentation Engineering

E-SANGATHI

Volume 10, issue 1
JAN 2025 - JUNE 2025

WHAT'S INSIDE

About JSS Mahavidyapeetha

The Jagadguru Veerasimhaasana Peetha was established in the 11th Century at Suttur by Adi Jagadguru Sri Shivarathreeshwara Shivayogi Mahaswamiji. JSSMVP was established in 1954 by 23rd Pontiff of Sri Suttur Math Jagadguru Dr. Sri Shivarathri Rajendra Mahaswamiji with the intension of providing “education for all.”. The Mahavidyapeetha has witnessed enormous growth in the field of education and has over 300 institutions under its fold. Presently the JSSMVP is presided by His Holiness Jagadguru Sri Shivarathri Deshikendra Mahaswamiji.

Website link: **https://jssonline.org**

ABOUT THE INSTITUTION

JSS Academy of Technical Education (JSSATE) was established in the year 1997 at Bengaluru and is under the umbrella of JSS Mahavidyapeetha, Mysuru. JSSATE has the vision of proactively participating in the establishment of a world class Institution for Technical Education. The Campus is located on a sprawling 21.17 acres of land surrounded by lush green plantations on the South-Western edge of Bengaluru City. The institution is affiliated to Visvesvaraya Technological University (VTU), Belagavi, India.More than 120 candidates are pursuing research programs at JSSATE, Bengaluru. The total strength of students is more than 2850.

Website link: **https://jssateb.ac.in**

From the Desk of Principal

It gives me immense pleasure to pen a few words as prologue to the 1st issue of Volume 10 of the biannual newsletter “E-SANGATHI” which is being released by the Department of Electronics and Instrumentation Engineering. One of the greatest strengths of the Department is its highly qualified and dedicated faculty members, who, apart from teaching, are engaged in research work. The objective of this newsletter is to churn out the latent writing talent which bears enormous potentiality of sharpening the communication skill of the students as well as to depict the different activities and achievements of the Department. I congratulate all the contributors and the editorial board, for materializing this issue of “E-SANGATHI”.

Dr. Bhimasen Soragaon

Message from HOD

I am extremely happy to present the first issue of volume Ten of our Department's newsletter-"E-SANGATHI". This is going to be a continuous process portraying the various academic as well as the non academic activities of our Department. We strive to be at the forefront of research, to educate our students in core fundamentals, and to engage students with emerging technologies and their applications. E&IE is a constantly advancing and widening branch of the engineering profession. It is one of the oldest branches which involves multidisciplinary aspects and also complies with the NEP2020 regulations. I, on behalf of the Department congratulate the editorial members, faculty and students.

Dr. K S Bhanumathi

FROM THE EDITORIAL DESK

With the blessings of His Holiness, Jagadguru Sri Sri Shivarathri Deshikendra Mahaswamigalavaru, It is indeed a great honour to present the Volume 10, Issue 1 of our department's biannual e-newsletter-E-SANGATHI. In this issue, we will recount the various activities and achievements of our students and faculty during the academic term January -June, 2025. Through our newsletter we aim to provide useful information to our stakeholders. We wish for your continued support in this regard, and a huge thank you to all those who contributed to this issue as these are essential to the newsletter's success.

VISION

To become globally competent in the field of Electronics and Instrumentation Engineering through Education, Research & Personality development

MISSION

1 Provide quality education by enriching students with technical knowledge and skills through holistic learning environment and state-of-the-art facility.

2. Encourage students to pursue higher studies and engage them in research and development

3. Instill professionalism and ethics among students who can strive to fulfill the needs of industry and society

FACULTY ACHIEVEMENTS

Training and Workshops Attended

Dr. Bhanumathi K S

1. Participated in a One Day Conclave titled "Semiconductors & Beyond: Building Powerful Computing Power", organized by the Department of Electronics & Communication Engineering, JSS University, Noida, in association with the University of Surrey, UK, on 9th April, 2025.
2. Completed a 15-day FDP on Artigicial Intelligence(AI), organized by SkillDzire in collaboration with AICTE, held from 17th to 31st March 2025.

Mrs. Harshitha S

Participated in a Professional Development Program (PDP) on “Scaling the Future: Trends and Challenges in Semiconductor Technology”, organized by the Department of ECE, JSS Science and Technology University, Mysuru, from 13th to 15th February 2025.

Mrs. Sowmya M S

1. Participated in the “Skill Sprout” 2-day Intensive Leadership Development Program, organized by JSS Academy of Technical Education, Bengaluru, on 9th and 10th January 2025.
2. Attended the topic “Energetic Dimensioning & 1 D simulation for Mahindra e20 EV” conducted at JSS ELECTRIC VEHICLE MOBILITY CENTER OF EXCELLENCE from 20th Jan to 24th Jan 2025.
3. Participated in a Professional Development Program (PDP) on “Scaling the Future: Trends and Challenges in Semiconductor Technology”, organized by the Department of ECE, JSS Science and Technology University, Mysuru, from 13th to 15th February 2025.
4. Completed a 15-day FDP on Internet of Things (IoT), organized by SkillDzire in collaboration with AICTE, held from 17th to 31st March 2025.

Mrs. Nagarathna N

1. Nagarathna N attended a six-day Faculty Development Program on Nanomaterials, organized by the AICTE Training and Learning (ATAL) Academy, Nelson Mandela Marg, Vasant Kunj, New Delhi, in collaboration with JSS Science and Technology University, Mysuru, held from 6th January 2025 to 11th January 2025.
2. Participated in the “Skill Sprout” 2-day Intensive Leadership Development Program, organized by JSS Academy of Technical Education, Bengaluru, on 9th and 10th January 2025.
3. Nagarathna N participated in a 15-day Faculty Development Program (FDP) on VLSI, organized by SkillDzire in collaboration with AICTE, held from 17th March 2025 to 31st March 2025.

Dr. Sharada Suresh Dambal

1. Successfully completed a 12-week NPTEL FDP online certification course on *"Digital Design with Verilog"*, organized by the Indian Institute of Technology Guwahati, through Swayam NPTEL, and endorsed by Skill India, conducted from January to April 2025.
2. Participated in a 6-day Faculty Development Program (FDP) on *Nanomaterials*, organized by the AI CTE Training and Learning (ATAL) Academy, Nelson Mandela Marg, Vasant Kunj, New Delhi, in collaboration with JSS Science and Technology University, Mysuru, held from 6th January 2025 to 11th January 2025.
3. Attended a 15-day Faculty Development Program (FDP) on *Artificial Intelligence*, organized by SkillDzire in collabo

Mrs. Shilpa H R

1. Participated in a Professional Development Program (PDP) on "Scaling the Future: Trends and Challenges in Semiconductor Technology", organized by the Department of ECE, JSS Science and Technology University, Mysuru, from 13th to 15th February 2025.
2. Attended an AICTE-ATAL Faculty Development Program (FDP) on "Advancement in Telehealth Industry using IoT", organized in collaboration with Lakshmaiah Education Foundation (Deemed to be University), off-campus at Aziznagar, Hyderabad, from 20th to 25th January 2025.
3. Completed a 15-day FDP on Internet of Things (IoT), organized by SkillDzire in collaboration with AICTE, held from 17th to 31st March 2025.
   participated in the "Skill Sprout" 2-day Intensive Leadership Development program, organized by JSS Academy of Technical Education, Bengaluru, on 15th and 16th January 2025.

Mr. Vijayakumara H S has successfully completed a 5-day online training program titled "Embedded for Beginners", conducted by the National Institute of Electronics and Information Technology (NIELIT), Calicut. This foundational course was held from 10th to 14th April 2025, offering participants an in-depth introduction to the fundamentals of embedded systems.

Our Technical staffs Mr.Nagenderaswamy M, Mr.CHANDAN N, Mrs. Deepa.A.Biradar, Mr.Shivaprakash B, Mr.Lokesh B and Mr.Mohan HP are attended a 3day workshop on Skill Enhancement. Organized by SJBIT, Bengaluru Held from 12-feb-2025 to 14-feb-2025






Student Achievements

Academic Year: 2025-26

Manya Bhat (USN: 1JS23EI027)

- Successfully completed The VLSI Design Course with a Grade B on 17th February 2025, and received a certificate issued by Scholiverse Educare Private Limited in association with Internshala Trainings, endorsed by Skill India and NSDC (National Skill Development Corporation).
- Successfully completed a 12-week NPTEL Swayam online course titled Microprocessors & Microcontrollers offered by the Indian Institute of Technology, Kharagpur, during January to April 2025.

Rohan Goswami (USN: 1JS23EI045)
Secured the Runner-up position in the category “Improve AQI Using AI” at the JSS Navotthana 2025 – 24-hour Hackathon, organized by the Department of CSE (AI & ML), JSS Academy of Technical Education, Bengaluru, held on 6th and 7th March 2025.

A. Apeksha (USN: 1JS23EI001)
Successfully completed a 12-week NPTEL Swayam online course titled Digital Design with Verilog offered by the Indian Institute of Technology, Guwahati, during January to April 2025.

Sakshi Patil (USN: 1JS23EI049)
Successfully completed a 12-week NPTEL Swayam online course on Digital Design with Verilog offered by IIT Guwahati, during January to April 2025.

Hrushikesh Upadhya (USN: 1JS22EI010)
completed a 12-week NPTEL Swayam online course on Digital Design with Verilog offered by IIT Guwahati during January to April 2025.

Lakshmi Sagar (USN: 1JS24EI024)
Completed an 8-week NPTEL Swayam online course on Introduction to Graphic Design offered by IIT Hyderabad, from February to April 2025.

Aryan Bhatt (1JS22EI004) and Malger Rachana Mudkappa (1JS22EI017) won the 2nd Runner-up position for their project titled "A Real-Time Sign Language Recognition System for Bridging Communications", as part of the Design & Innovation Clinic 2025 competition organized by the Central Manufacturing Technology Institute (CMTI), held from 7th to 9th April 2025 at the CMTI campus, Bengaluru.

Aryan Bhatt

Malger Rachana Mudkappa

University Ranks

Pratheeth Pakka bearing USN 1JS21EI033 secured 5th Rank in VTU among the 2025 outgoing batch of Electronics & Instrumentation Engineering at JSS Academy of Technical Education, Bengaluru, announced on 1st July 2025.

B. Manjunath
bearing USN 1JS21EI008 secured 7th Rank in VTU among the 2025 outgoing batch of Electronics & Instrumentation Engineering at JSS Academy of Technical Education, Bengaluru, announced on 1st July 2025.

Ryna Bhatt bearing USN 1JS21EI038) secured 6th Rank in VTU among the 2025 outgoing batch of Electronics & Instrumentation Engineering at JSS Academy of Technical Education, Bengaluru, announced on 1st July 2025.

Funding

he project proposal titled “Tracksure: Advanced Real-time GPS and RFID Tracking System with Geofencing and SOS Features”, submitted by the student group **Anushree N (1JS21EI007), Rakshitha Guru (1JS21EIO37), and C.T. Valliammai (1JS21EI010)** under the guidance of **Dr. Sharada S D,** received sponsorship under the **48th Series of Student Project Programme (SPP) by the Karnataka State Council for Science and Technology (KSCST),** IISc Campus, Bengaluru, for their project sanctioned with a grant of ₹4,500 on 14th March 2025.

Anushree N

Rakshitha Guru

Valliammai .C.T

INTERNSHIP

SIEMENS-15KPM

Chandana G. Hublikar
(1JS21EI011)

Hemalatha S
(1JS21EI023)

NOUVEAU KABS PVT.LTD.-15KPM

Sanjana Devaraj Hegde
(1JS21EI039)

KONIGTRONICS(OPC) PVT.LTD-15KPM

Nabha Hoovappa
(1JS21EI029)

DRDO-8KPM

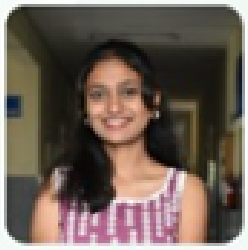
Abhyudaya Nayak
(1JS21EI001)

M2NXT-15KPM

Achyuth Kashyap
(1JS21EI002)

KIET TECHNOLOGIES-5KPM

Shashank B G
(1JS21EI042)



Ananya A Kashyap
(1JS21EI004)

EVENTS ORGANISED IN THE DEPARTMENT

Workshop titled "Introduction to the Semiconductor Industry"

Workshop on “Introduction to the Semiconductor Industry” – JSSATE, Bengaluru
The Department of Electronics & Instrumentation Engineering at JSSATE, Bengaluru, in collaboration with the Instrument Society of India and Infineon Technologies, organized a workshop titled “Introduction to the Semiconductor Industry” on May 5th, 2025.

The workshop aimed to provide students with insights into the semiconductor industry, emphasize the importance of STEM education, and bridge academia–industry gaps. Mr. Balasubramanian Prakash, Distinguished Principal & System Architect at Infineon, introduced the company and highlighted career prospects in the sector.

The event was graced by Dr. Rekha P M (Dean – Academics), Dr. D Maheshkumar (Dean – Student Welfare), and Dr. K S Bhanumathi (HoD, E&IE). Technical sessions included expert talks by Mr. Prakash on STEM relevance, and experience sharing by Infineon engineers Mr. Boya Vinay Kumar and Mr. Bagewadi Sharvil Vijayanand. Mr. Balajee Sheshadri also delivered a talk on modern electronics.
The session saw active participation from E&IE students and faculty and was efficiently coordinated by Mrs. Sowmya M S, Assistant Professor, Dept. of E&IE.

Talk on “GROWTH MINDSET IN ENGINEERING STUDENTS” held on 26th April, 2025

On April 26, 2025, an invited seminar on "Growth Mindset in Engineering Students" was hosted by the Department of Electronics & Instrumentation Engineering. The speaker was Mr. Naveen Chandra, Global Product Manager of ABB Global Industries and Services Ltd. and a department alumnus. The program was attended by the fourth, sixth, and eighth semester students of the Dept. The main traits of a growth mindset in engineering students were emphasized by the speaker. Additionally, he highlighted the advantages of developing a growth mindset. He added that rather than being innate qualities, our brains and skills may be enhanced with commitment and effort. Students who adopt this perspective are more likely to rise to difficulties, persevere through setbacks, and see failures as teaching moments.
Mrs. Harshitha S, Assistant professor in the Department of E&IE, planned and oversaw the program. We sincerely thank Mr. Naveen Chandra for accepting our invitation and sharing his expertise with our students. We also thank all of the E&IE Department staff and students for their enthusiastic involvement in helping to make this event a success.

ISOI Student Chapter Inauguration and expert talk on “Beating the Blood Curse: Deploying Science to Defeat Sickle Cell Anemia

The Department of Electronics & Instrumentation Engineering (E&IE), JSS Academy of Technical Education, Bengaluru, officially inaugurated the Instrumentation Society of India (ISOI) Student Chapter on 25th April 2025. This initiative aims to provide students a professional platform to engage with the evolving world of instrumentation and technology.

The event was honored by Mr. Hanukumar (Program Manager, ISOI, IISc), Dr. Rajesh Srinivasan (Scientist, IISc), Dr. Rekha P M (Dean – Academics), Dr. D Mahesh Kumar (Dean – Student Welfare), Dr. Bhanumathi K S (HoD, E&IE), and Mrs. Sowmya M S (ISOI Coordinator). Faculty and students participated enthusiastically.

Dr. Rajesh Srinivasan delivered an expert talk on “Beating the Blood Curse: Deploying Science to Defeat Sickle Cell Anemia”, highlighting gene therapy and diagnostic technologies. A hands-on demonstration of a diagnostic kit provided practical insights into disease detection, linking engineering and healthcare.

The event was coordinated by Mrs. Sowmya M S, whose efforts ensured its success. Appreciation is extended to Mr. Hanukumar for his vital support in establishing the ISOI chapter. Membership was extended to 4th and 6th semester E&IE students.

The entire event was thoughtfully planned and smoothly executed under the coordination of Mrs. Sowmya M S, Assistant Professor who played a pivotal role in organizing the session and ensuring its success.A special note of appreciation goes to Mr. Hanukumar for his continuous support, encouragement, and facilitation in establishing the ISOI Student Chapter and enabling this fruitful collaboration. The membership was taken to 4th and 6th semester of E&IE, JSSATEB.

“Machine Learning Tools for Biomedical Instrumentation” workshop on 8/04/2025

The Department of Electronics and Instrumentation Engineering, JSSATEB, organized a hands-on session on "Machine Learning Tools for Biomedical Instrumentation" on Tuesday, 8th April 2025 at the Library Seminar Hall, Block B. The session began at 2:00 PM with a welcome address by Dr. Sharada S Dambal, followed by the introduction of the expert speaker Dr. Mallikarjun by Dr. Bhanumathi K S, HoD, E&IE.

Dr. Mallikarjun explained core machine learning tools and demonstrated their application in biomedical instrumentation, including disease classification models. The session included practical demonstrations that enhanced participants’ understanding and skills. Students from 4th and 8th semesters, faculty members, and technical staff actively participated.

The workshop concluded at 4:00 PM and received highly positive feedback. It was successfully coordinated by Dr. Sharada Suresh Dambal, Assistant Professor,E&IE.

A seminar on “Road to IITs & PSU Job through GATE” was organized by the Department of Electronics and Instrumentation Engineering on Thursday, 20th March, 2025 at JSSATEB, by Mr. Vipin Kumar Mishra, Lead Mentor at IMS GATE Academy, and Mr. Sanjeev Kumar, Manager-Student Awareness IMS Learning Resources Pvt Ltd, Bengaluru was the guest lecturer of the seminar.

The objective of this seminar was to guide students on the path to securing admissions in IITs and obtaining PSU jobs through the GATE examination.

This seminar was attended by 4th and 6th semester students of Electronics and Instrumentation Engineering and received positive feedback from all participants. The session was fruitful for students and has given strong insights on the PSU Jobs and other exams. This program was organized and coordinated by Mrs. Nagarathna N, Asst. Prof. Dept. of E&IE.

“Road to IITs & PSU Job through GATE” seminar on 20/03/2025

A seminar on “Road to IITs & PSU Job through GATE” was organized by the Department of Electronics and Instrumentation Engineering on Thursday, 20th March 2025 at JSSATEB. The session was conducted by Mr. Vipin Kumar Mishra, Lead Mentor at IMS GATE Academy, and Mr. Sanjeev Kumar, Manager – Student Awareness, IMS Learning Resources Pvt. Ltd., Bengaluru, who was the guest lecturer for the event.

The primary objective of this seminar was to enlighten students on effective strategies to excel in the GATE examination, thereby opening pathways to higher education in premier institutes like IITs and securing prestigious positions in Public Sector Undertakings (PSUs).

The seminar was attended by students of the 4th and 6th semesters of the Electronics and Instrumentation Engineering department. The speakers provided valuable insights on GATE preparation techniques, exam patterns, time management strategies, and the career opportunities available through GATE. They also highlighted the relevance of PSU jobs, eligibility criteria, and selection processes, along with alternative competitive examinations in the engineering domain.

Participants actively engaged with the speakers, asking questions and seeking guidance for their career goals. The session received positive feedback from all attendees, who found it highly informative, motivational, and aligned with their aspirations.

The program was meticulously planned, organized, and coordinated by Mrs. Nagarathna N, Assistant Professor, Department of E&IE. Overall, the seminar proved to be a fruitful and inspiring experience for students, equipping them with a clearer vision for their academic and professional journey.

BIOMETRIC FORENSIC TOOLS

Abstract

Biometrics, such as fingerprints, facial recognition, and iris patterns, vary from person to person, making them valuable for crime investigations. These unique biological traits provide accurate evidence that can help identify suspects. The use of biometric forensic tools has assisted in solving complicated cases as well as cold cases. Advances in technology have significantly enhanced their effectiveness in crime-solving.

Keywords

Biometric forensic tool, facial recognition, crime investigations, iris scanner, fingerprint scanner

Introduction

The term "biometric" originates from the Greek words for "life" (bio) and "to measure" (metric). Therefore, biometrics can be understood as the measurement of human characteristics. While all humans share a similar anatomy, there are slight variations among individuals. These variations are what enable the use of biometric forensic tools, such as hair analysis, iris recognition, and fingerprints. Biometric forensic tools can be further classified into two **categories:**

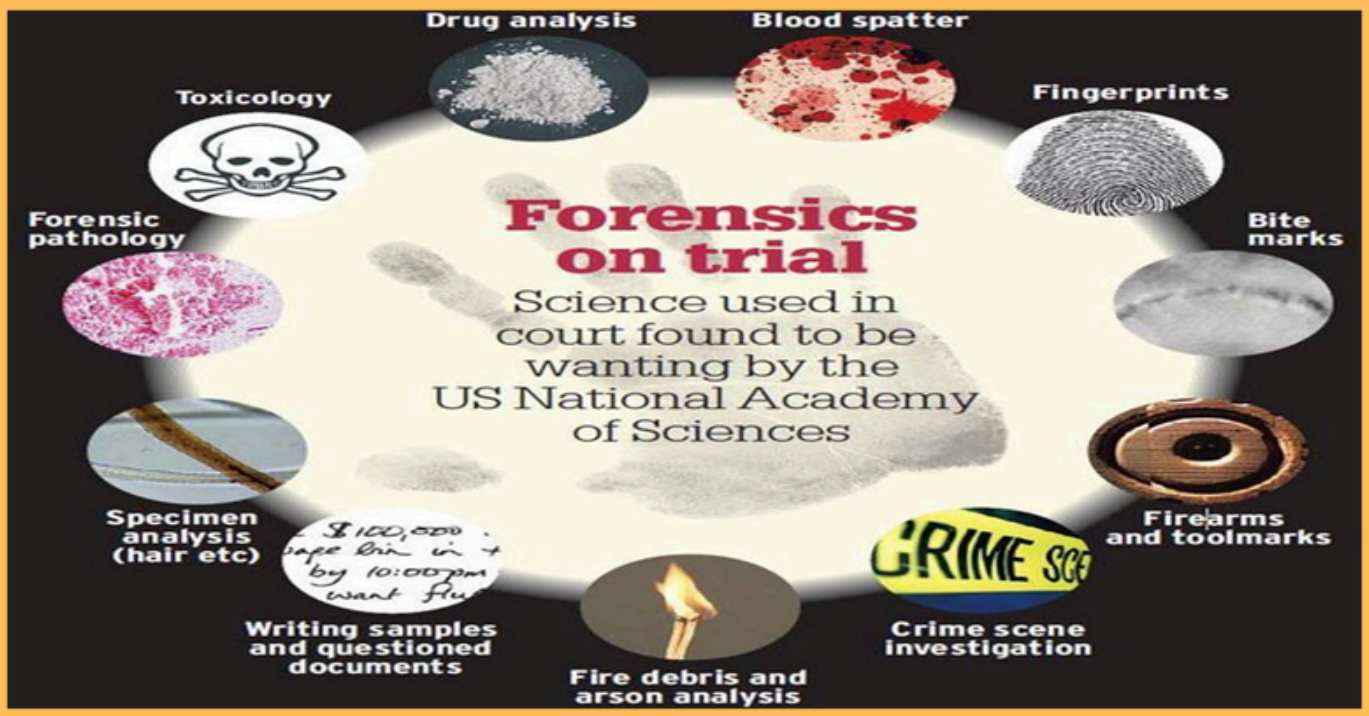
Physiology biometrics :

Physiological parameters are stable and do not change over time, such as fingerprints, facial recognition, and iris recognition. Physiological biometrics are typically morphological or biological, and therefore cannot be altered throughout an individual's life.

Behavioral biometrics:

Behavioral biometrics is quite different from physiological biometrics. It focuses on identifying patterns in how a person behaves or interacts. These patterns include signatures (which can change over time), keystroke dynamics (such as typing rhythm or speed), and voice recognition, among others. These factors can be influenced by various elements, including changes in the environment, mood, and health.

Types of biometric forensic tools



The biometric forensic tools can be classified into

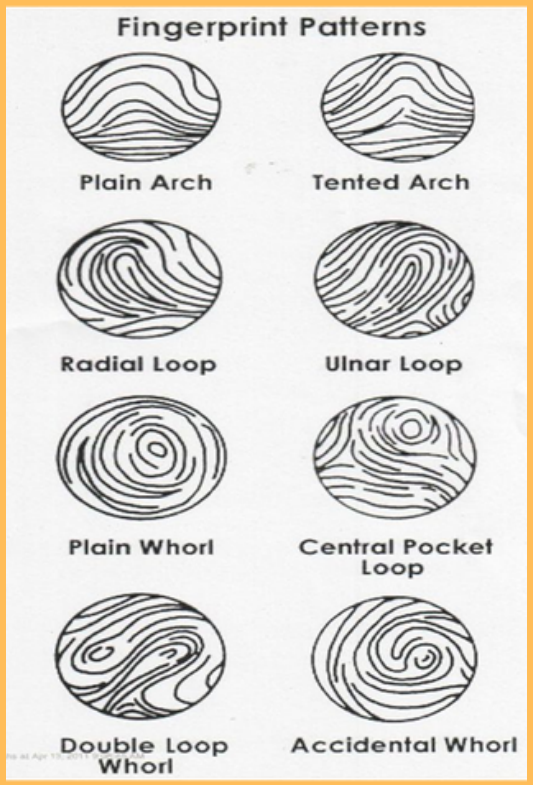
●Fingerprint analysis tool

Fingerprint analysis has played a vital role in biometric forensics, assisting in solving many challenging crimes during investigations. Although individuals may have similar finger shapes, the unique variations in fingerprints create distinct patterns. The FBI maintains an Integrated Automated Fingerprint Identification System (IAFIS), which is a computer-based system that stores, matches, and compares fingerprints. This system was introduced in 2010.

●Iris recognition

The eye has two main parts: the retina and the iris. The iris typically has around 266 unique features, which leads to much greater variability compared to other traits where there are only about 13 to 16 variations. This uniqueness has made iris recognition a valuable tool in the field of biometric forensics. Additionally, the iris remains stable throughout a person's life and does not change with age. As a result, iris recognition is widely used in passports, hospitals, access security, and other applications.

In 2002, the Iris Recognition Immigration System was introduced, making it easier and faster for passengers flying through British airports compared to the old passport technology.



●Gait biometrics

Gait, also known as stride, refers to a person's unique walking pattern. This pattern can change over time and varies from one individual to another. Because of these differences, gait analysis is useful in forensic investigations. However, factors like body weight, clothing, footwear, and the surrounding environment can affect how a person walks.

Gait biometrics are usually captured using video recordings, which means advanced computer-based recognition systems are needed to analyze the data. Some of the key features studied in gait analysis include step length, walking rhythm, bounce, and other movement patterns.

● Keystrode biometrics

Keystroke biometrics refers to the way an individual types on a keyboard, such as their typing speed and rhythm. Compared to other biometric methods, it is less common but a simple and cost-effective technique since it only requires a keyboard and special software.

The typing pattern can vary based on factors like typing speed, emotional state, and the type of keyboard used. Since keystroke biometrics are behavioral, they can change over time as a person's habits or conditions change.

Methodology

In this biometrics forensic tool it has some steps those are

●**Data:** Collecting the data such as iris, facial features, fingerprints, gait, etc. As we know, we all have different variations, so this helps in identifying victims by connecting the data to sensors and appropriate collection kits.

●**Preprocessing:**

In this step, it processes the data such as iris, fingerprints, palm patterns, etc. The preprocessing helps in removing unwanted signals and further prepares the data for analysis.

●**Feature extraction:**

After removing all the unwanted signals from the data, the characteristics have to be identified and extracted, such as DNA sequences, facial features, etc.

●**Template creation:**

After extracting the features, they are displayed in the form of digital data, which is a more compatible form.

●**Matching and comparison:**

The known data is compared with the existing data already stored.

●**Decision making:**

After comparing, the decision is made by analyzing the data to identify if there are any similarities.

●**Reporting:**
If there are any similarities, the findings are filed for legal proceedings.
Conclusion
Biometric forensic tools are very helpful in solving crimes because they use unique body features like fingerprints and face patterns to identify people. These tools collect and compare data to find matches, which helps police find suspects and victims. Even though there are some problems like privacy and data mistakes, biometric tools are getting better and are important for justice.

Reference

●Sachil kumar,geetika saxena “the biometric tools for criminal investigations”october 18,2021
● Mokopane Charles Marakalala Corresponding authour: marakmc@unisa.ac.za College of Law, School of Criminal Justice, Department of Police Practice, University of South Africa, Preller Street Muckleneuk Ridge, Pretoria, South Africa “Forensic Identification: The Biometric Technology Linked to Online Financial Fraud and Crime Related to the South African Banking Industry”

Cyanotype printing:
The art of capturing light on paper painting made by the sun

Cyanotype painting is an art where it doesn't use normal paints,crayons, and brushes. Cyanotype painting is where natural materialsare used, such as leaves and flowerson the coated material. When it is exposed to the sunlight, it gives a magical whiteshadow on the material. It's a magical sun photograph where it captures a white shadow

What is cyanotype painting?

Cyanotype is a form of art and this process is also called the blueprintprocess, where it uses unique things such as leaves and flowers it uses UV light, and light-sensitive Chemicals as we know it doesn't use brushes, paintand crayons .herethe flowers, leavesare placed on the
coatedpaper and when exposed to the sunlightit gives a magical white shadow surrounded byan magical blue background

History

Cyanotype painting also known as the blueprintprocess, was first introduced by John Herschel in 1842.
In the early,the paper was coated with iron saltsand then used in contactprinting then the paper was washed and gave a blueish background and white shadow of an object Herschel also gave us the photography, negative,positive, and snapshots
Oneof the people to bringthe cyanotype process,Anna Atkins, in October 1843,became the first person to produce the first book to use photographic illustrations.

Materials used

Cyanotype solution (ferricammonium citrate and potassium ferricyanide) Thick paper or fabric
Objects for printingsuch as leavesand flowers Sunlight or UV lamp
Water

How to make

Prepare the paper
Take a thick paper or a clothand coat it with the cyanotype solution

Arrange the material

Arrange the materialsuch as flowersand leaves on the coatedmaterial in a such way that it gives aesthetic design

Sunlight

Take materials and keep them under the sunlightor UV light

Washing process

Remove the flowersand leaves on the paper or the fabric and wash them in the water as it dries we can see the blue background and white shadow of the material
By following thesesteps, you can create your stunning cyanotype prints that are beautifully light and the essence of nature

Sinchana B
E&ie 2nd year

EDITORIAL BOARD